MAIL ROOM
JUN 10 1994
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#20/Formal Drawing
R. Morgan
6/16/94

780.29743X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Thomas J. CAMPANA, Jr., et al.
Serial No.:	07/702,939
Filed:	May 20, 1991
For:	ELECTRONIC MAMIL SYSTEM WITH RF COMMUNICATIONS TO MOBILE PROCESSORS AND METHOD OF OPERATION THEREOF
Group:	2608
Examiner:	G. Oehling

TRANSMITTAL OF FORMAL DRAWINGS

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

Enclosed are (12) sheets of formal drawings, showing Figs. 1-12, in connection with the above-identified application.

Respectfully submitted,

Donald E. Stout

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Registration No. 26,422

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5436960

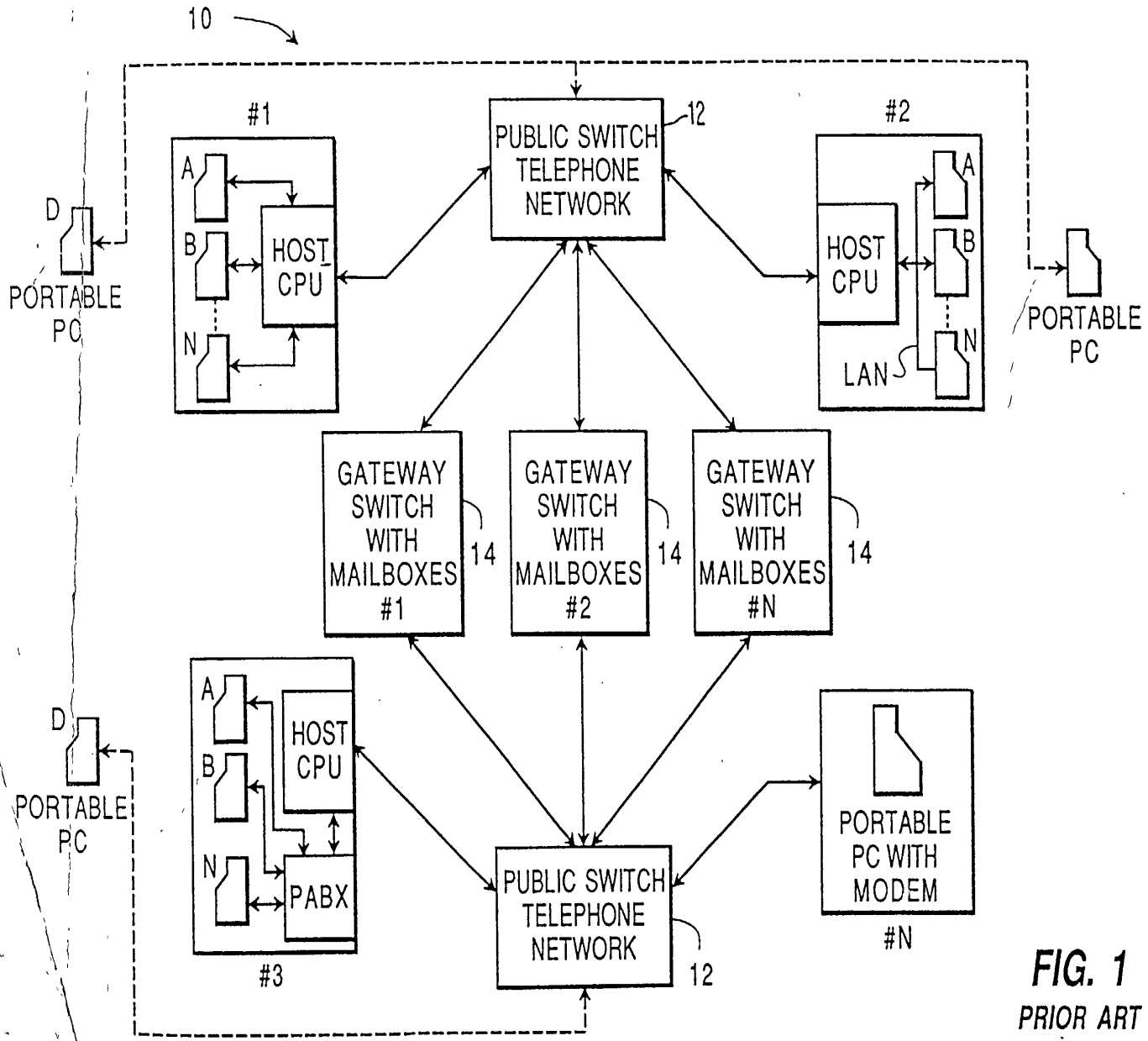


FIG. 1
PRIOR ART

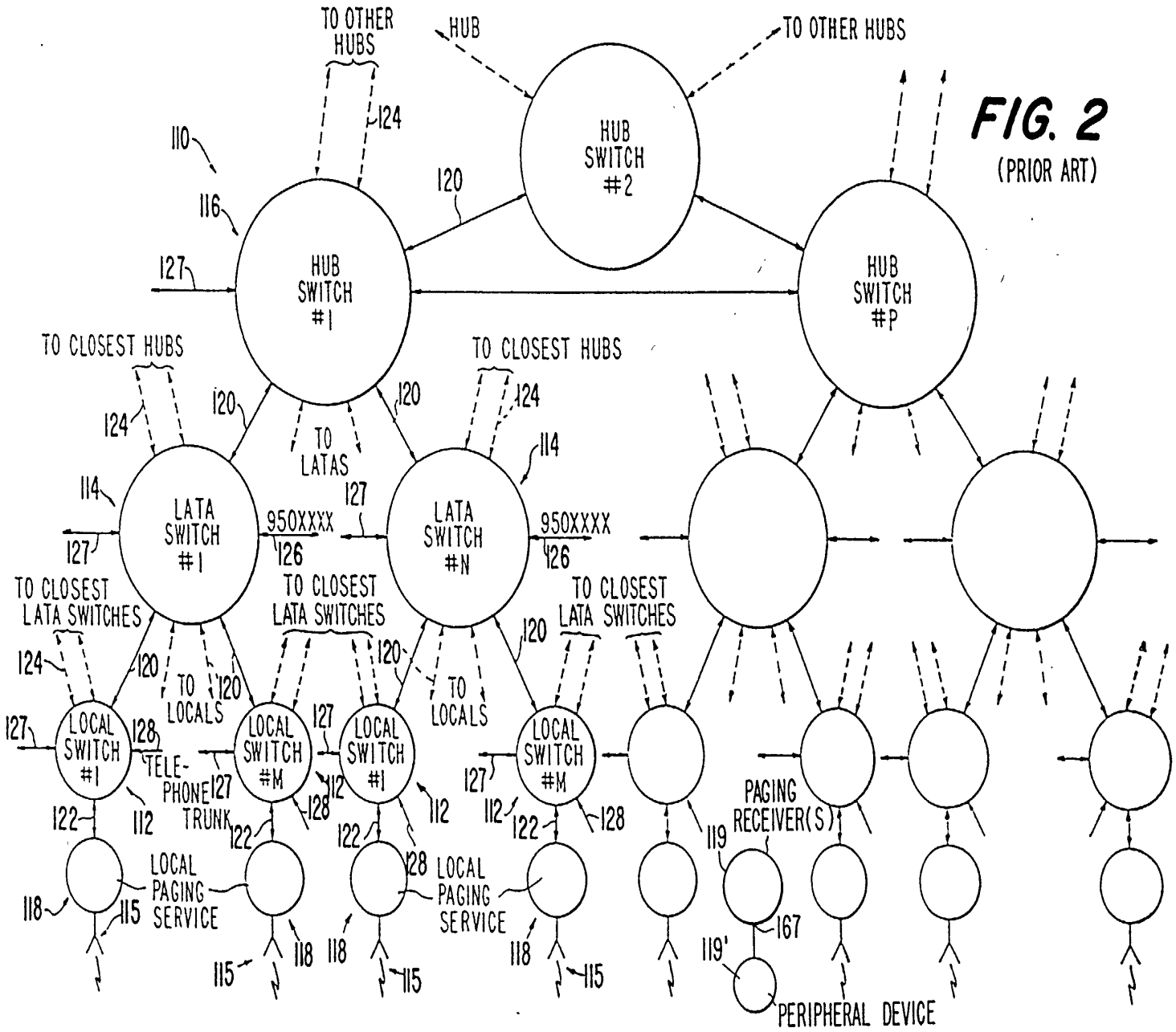


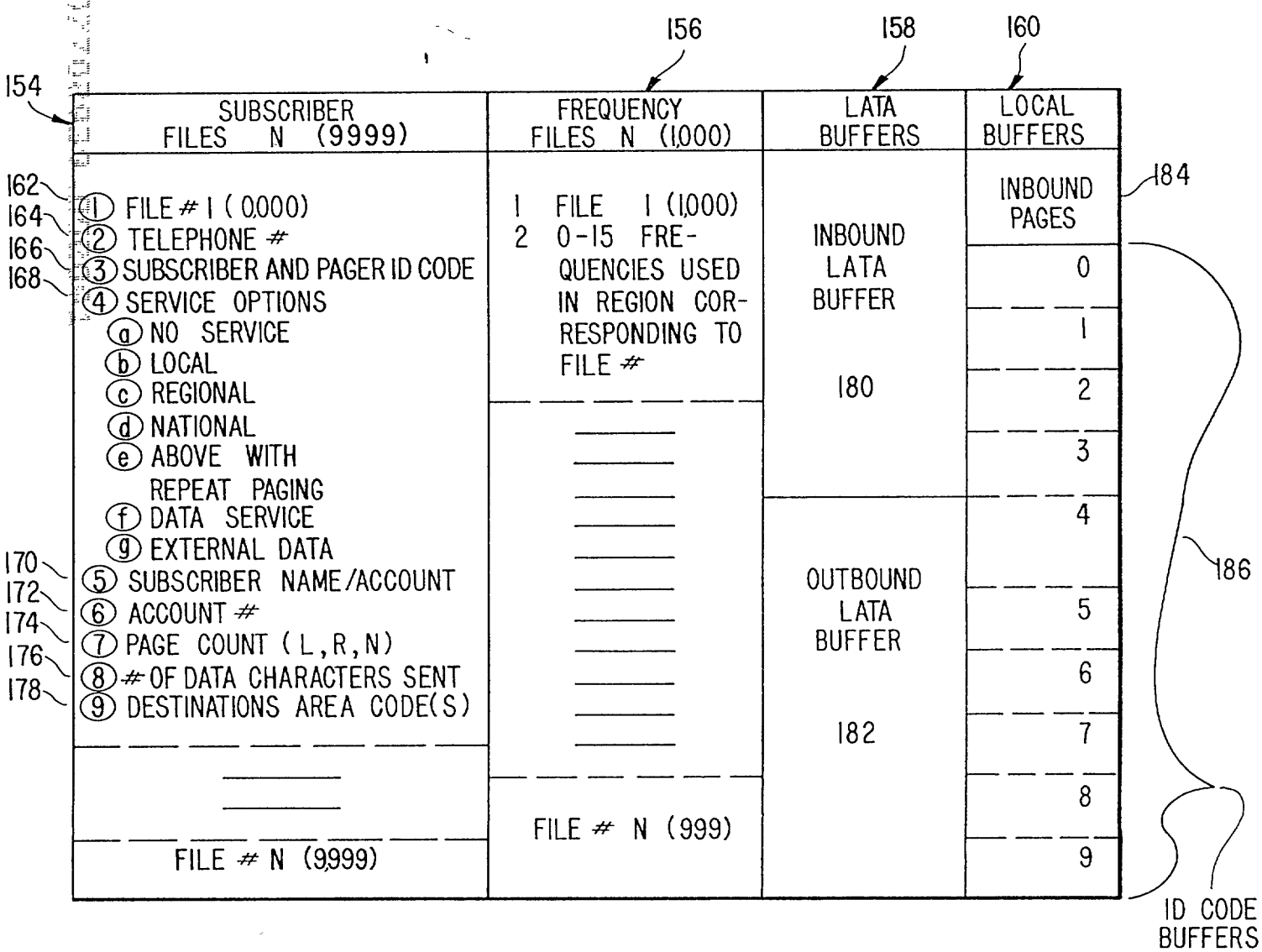
FIG. 2
(PRIOR ART)

E.G. FIG.
CLASS SUBCLASS

FIG. 3

(PRIOR ART)

LOCAL SWITCH MEMORY MAP

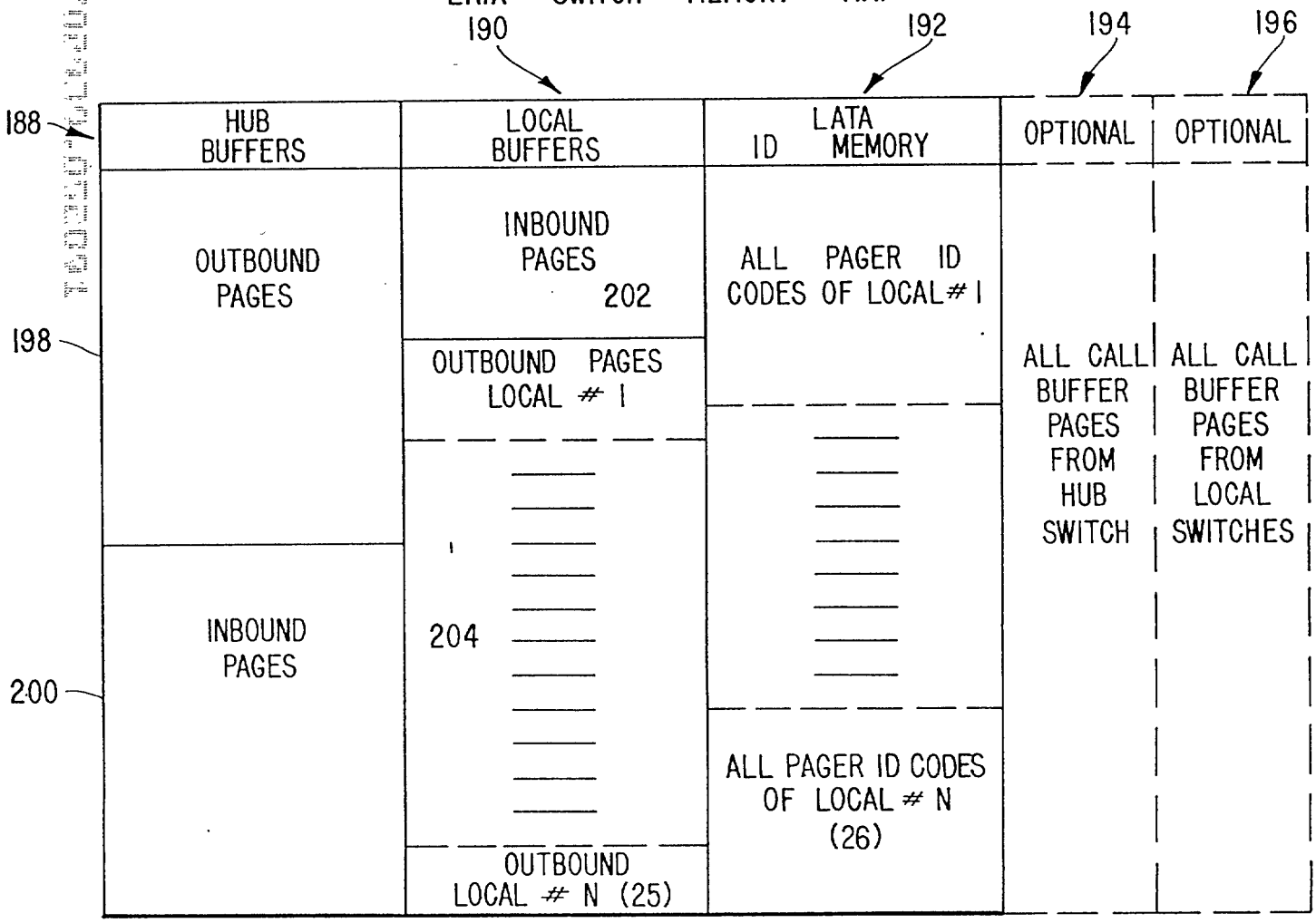


O.G. FIG.
CLASS | SUBCLASS

FIG. 4

(PRIOR ART)

LATA SWITCH MEMORY MAP

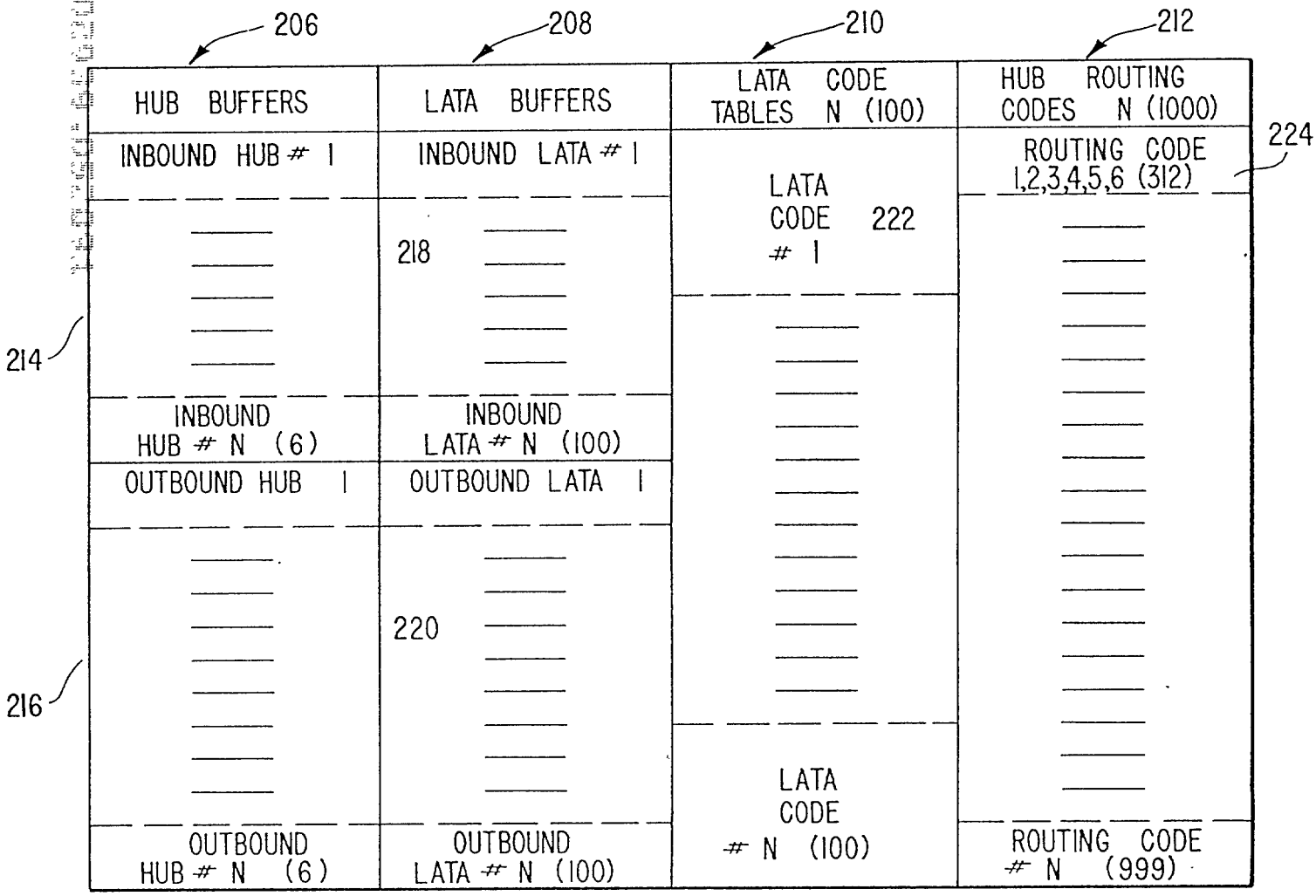


O.G. FIG.
CLASS SUBCLASS

FIG. 5

(PRIOR ART)

HUB SWITCH MEMORY MAP

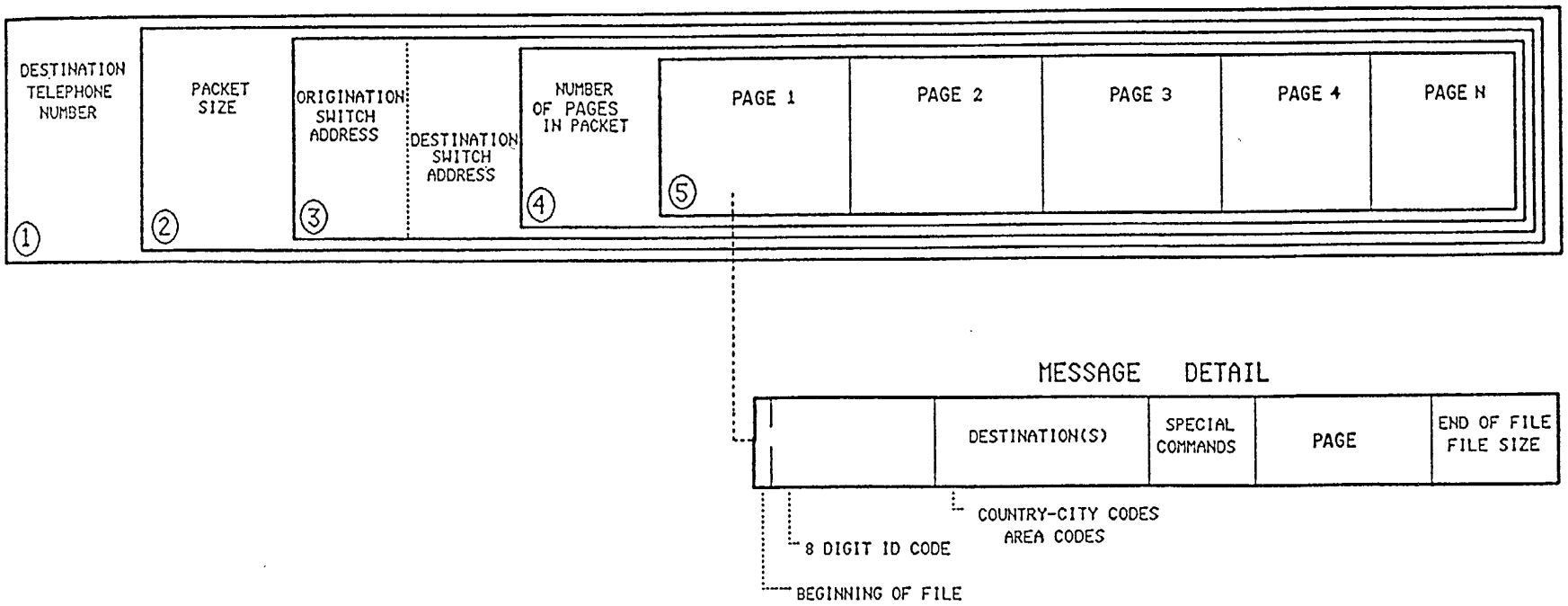


TO:3250* 05620128

FIG. FIG. CLASS SUBCLASS

FIG. 6
(PRIOR ART)

THE FIVE LAYER MODIFIED X.25 PACKET



O.G. FIG.
PLACES | SUBCLASS

FIG. 7
PRIOR ART

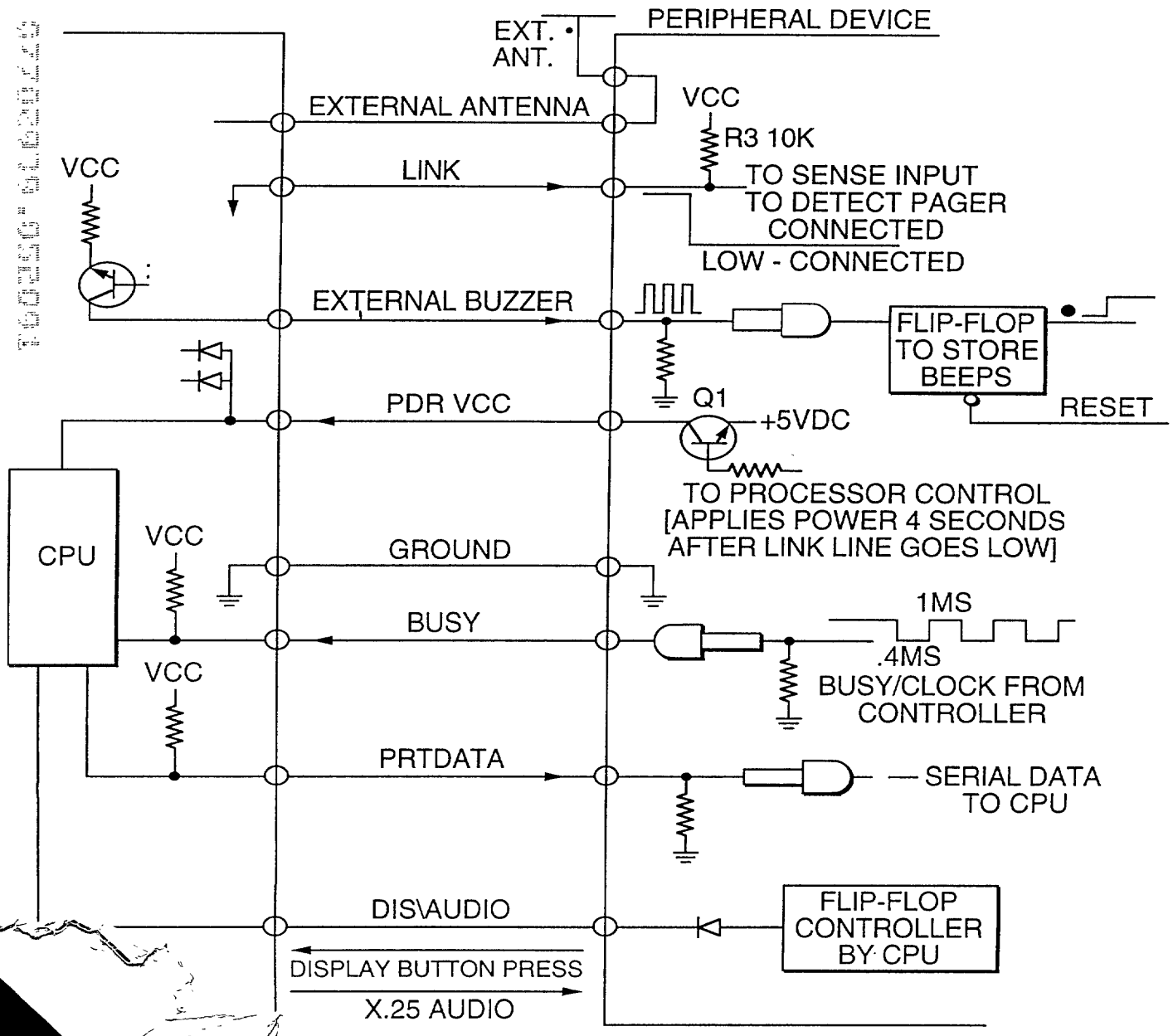


FIG. 8

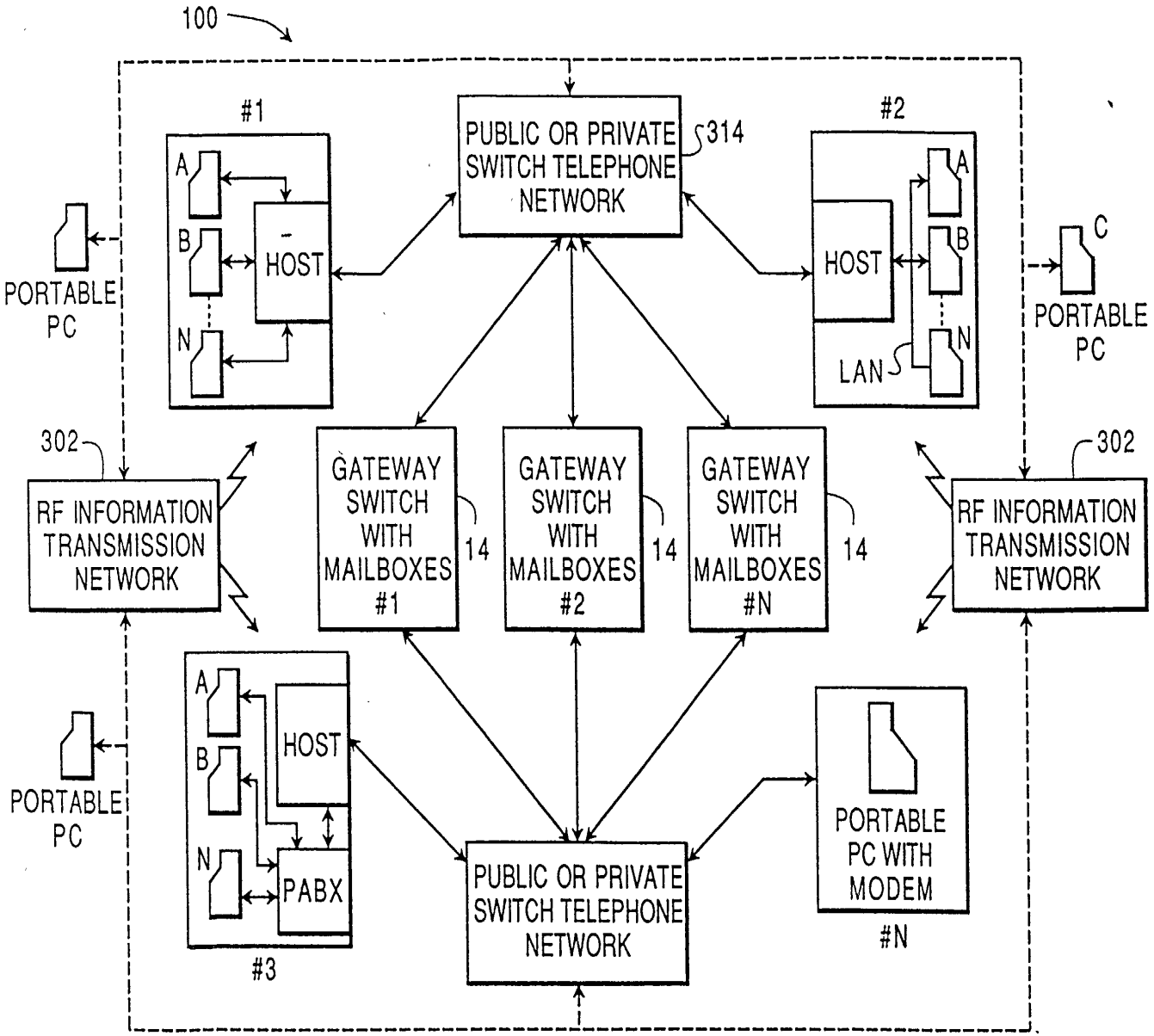

100
#1
PUBLIC OR PRIVATE SWITCH TELEPHONE NETWORK
314
#2
A
B
N
HOST
HOST
A
B
N
LAN
PORTABLE PC
C
PORTABLE PC
302
302
RF INFORMATION TRANSMISSION NETWORK
GATEWAY SWITCH WITH MAILBOXES #1
14
GATEWAY SWITCH WITH MAILBOXES #2
14
GATEWAY SWITCH WITH MAILBOXES #N
14
RF INFORMATION TRANSMISSION NETWORK
PORTABLE PC
A
B
N
HOST
PABX
#3
PUBLIC OR PRIVATE SWITCH TELEPHONE NETWORK
PORTABLE PC WITH MODEM
#N

FIG. 9

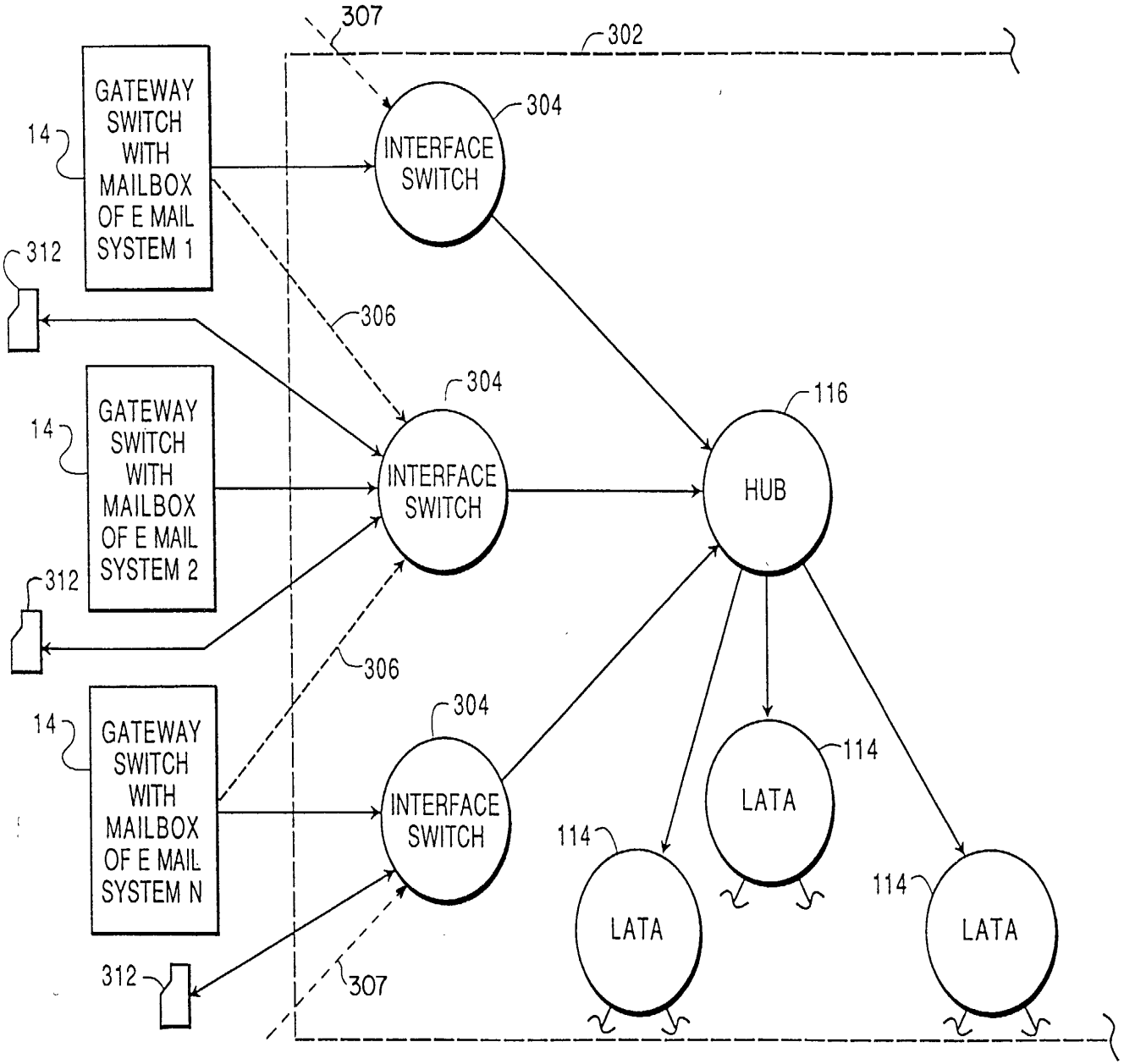
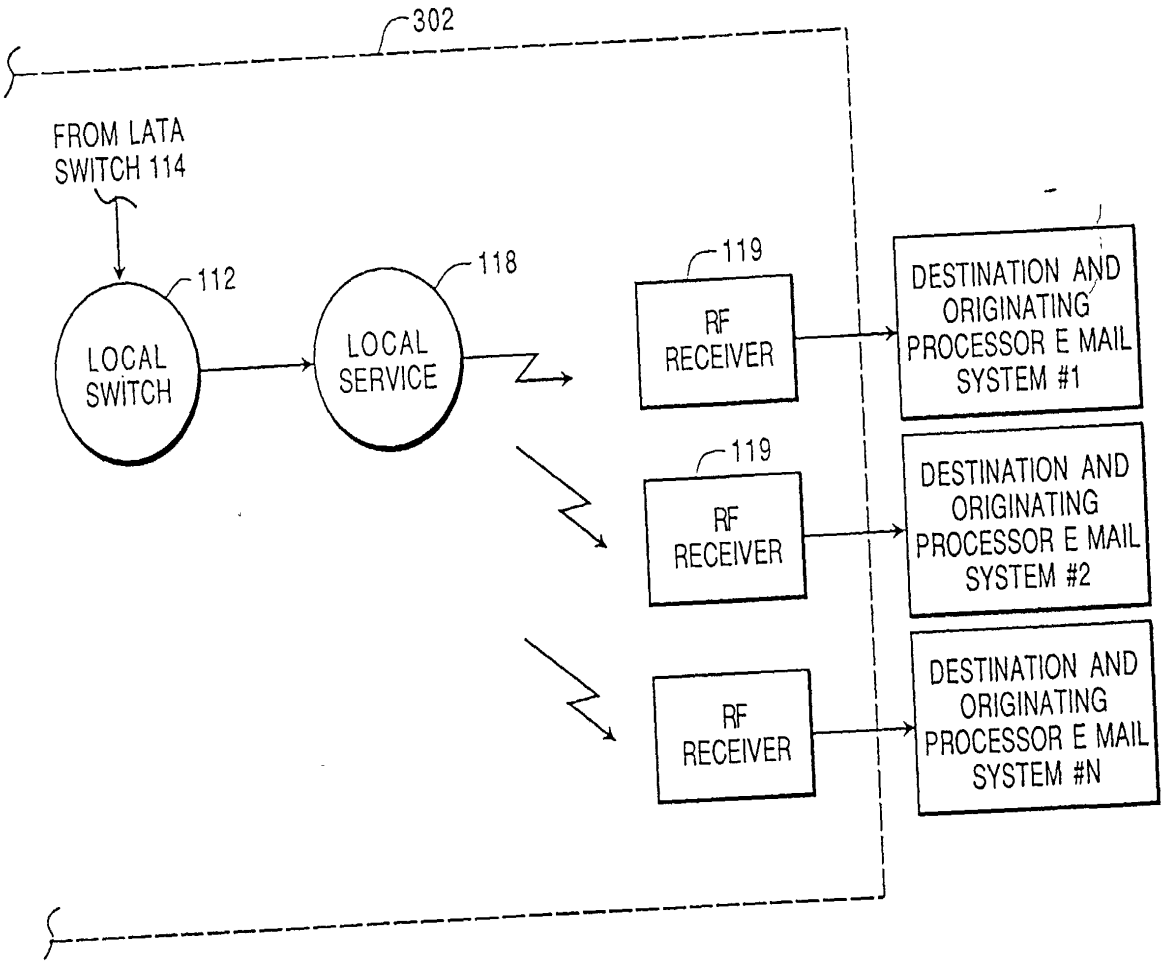

307
302
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM 1
14
312
INTERFACE SWITCH
304
306
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM 2
14
312
304
INTERFACE SWITCH
116
HUB
306
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM N
14
312
304
INTERFACE SWITCH
307
114
LATA
114
LATA
114
LATA

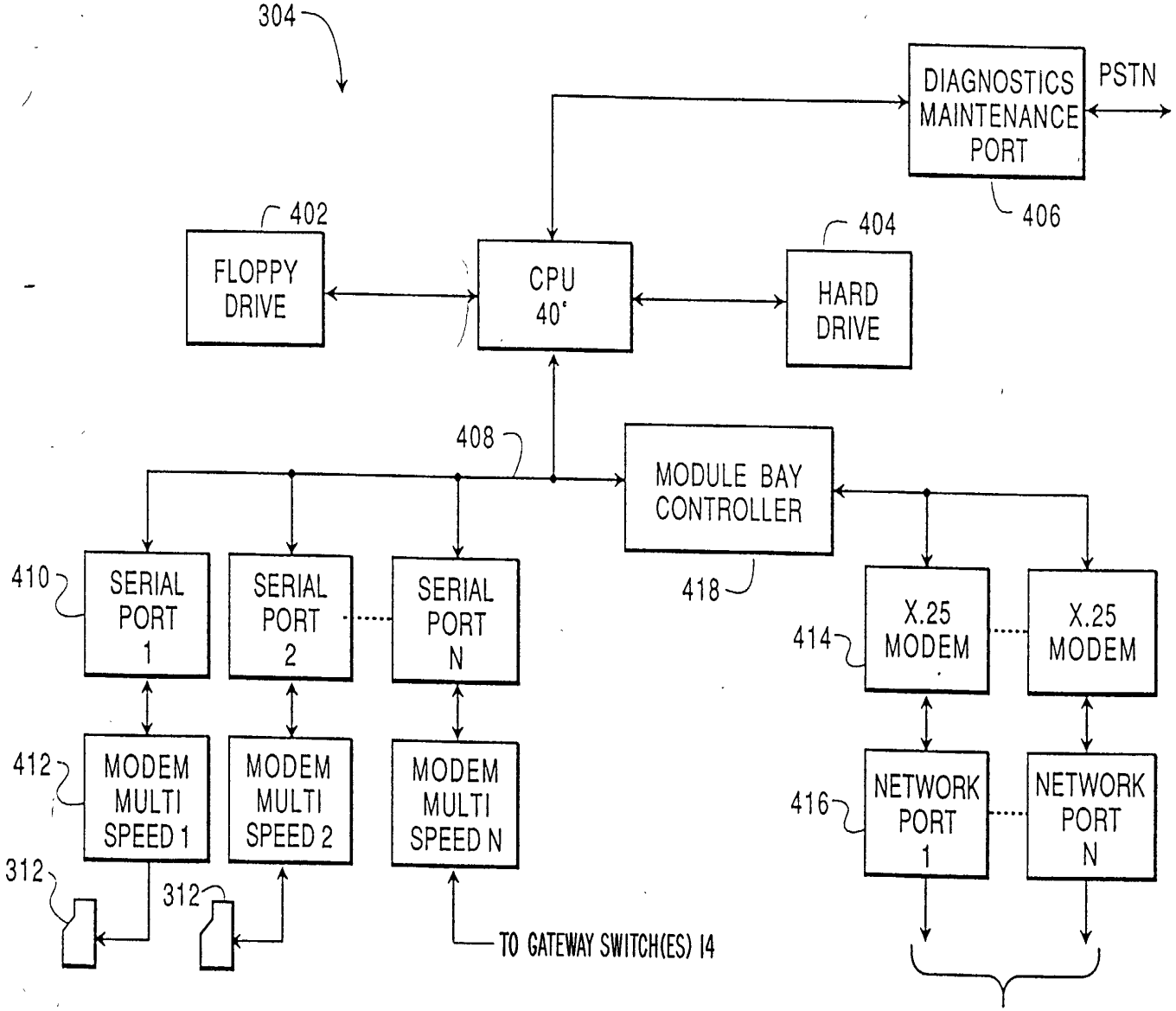
FIG. 10


302
FROM LATA SWITCH 114
112
LOCAL SWITCH
118
LOCAL SERVICE
119
RF RECEIVER
119
RF RECEIVER
RF RECEIVER
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #1
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #2
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #N

ENTRY METHOD	ORIGINATING PROCESSOR	GATEWAY SWITCH 14	INTERFACE SWITCH 304
1	ADDS INTERFACE (WIRELESS) DESTINATION AND DESTINATION PROCESSOR	NO-ACTION	ADDS ID OF RF RECEIVER 119
2	ADDS INTERFACE (WIRELESS) DESTINATION AND ID OF RECEIVER 119	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION
3	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION	ADDS ID OF RECEIVER 119
4	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
5	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
6	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	NO-ACTION	ADDS ID OF RECEIVER 119
7	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119(BY COMPARING DESTINATION PROCESSOR TO ID TABLE).	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION

FIG. 11

FIG. 12


304
DIAGNOSTICS MAINTENANCE PORT
PSTN
406
402
FLOPPY DRIVE
CPU 40'
404
HARD DRIVE
408
MODULE BAY CONTROLLER
418
410
SERIAL PORT 1
SERIAL PORT 2
SERIAL PORT N
412
MODEM MULTI SPEED 1
MODEM MULTI SPEED 2
MODEM MULTI SPEED N
312
312
TO GATEWAY SWITCH(ES) 14
414
X.25 MODEM
X.25 MODEM
416
NETWORK PORT 1
NETWORK PORT N